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wherein the pixel array, driver and control unit are formed simultaneously.

C2
9. (Amended) A method of fabricating a system-on-panel typed liquid crystal display,

comprising:

providing a substrate including at least first, second and third regions;

forming a pixel array on the substrate in the first region;

forming a driver on the substrate in the second region;

forming a control unit on the substrate in the third region, wherein the control unit includes switching devices having at least one active layer formed of single crystalline silicon; and

wherein the pixel array, driver and control unit are formed simultaneously.

C3
19. (Amended) A method of fabricating a system-on-panel liquid crystal display,

comprising:

providing a substrate including at least first, second and third regions;

forming a pixel array on the substrate at the first region, the pixel array having an active layer including amorphous silicon;

forming a driver on the substrate at the second region, the driver having an active layer including polysilicon or single crystalline silicon;

forming a control unit on the substrate at the third region, the control unit having an active layer including polysilicon or single crystalline silicon, wherein the control unit includes switching devices having at least one active layer; and

wherein the pixel array, driver and control unit are formed simultaneously.

21. (Amended) A method of fabricating a system-on-panel liquid crystal display,
comprising:

providing a substrate including at least first, second and third regions;

forming a pixel array on the substrate at the first region, the pixel array having an active layer including amorphous silicon;

forming a driver on the substrate at the second region, the driver having an active layer including polysilicon or single crystalline silicon;

forming a control unit on the substrate at the third region, wherein the control unit includes switching devices having at least one active layer formed of single crystalline silicon; and

wherein the pixel array, driver and control unit are formed simultaneously.